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PTO/SB/82 (10-00)

Approved for use through 10/31/2002. OMB 0651-0035

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**REVOCATION OF POWER OF
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AUTHORIZATION OF AGENT**

Application Number	09/966,967
Filing Date	9/28/01
First Named Inventor	Xueping Jiang
Group Art Unit	2121
Examiner Name	
Attorney Docket Number	7023-005

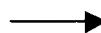
I hereby revoke all previous powers of attorney or authorizations of agent given in the above-identified application:

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Applicant/Inventor.

Assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)

SIGNATURE of Applicant or Assignee of Record

Name	Mark L. Becker
Signature	
Date	9/23/02

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.

*Total of _____ forms are submitted.

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PTO/SB/81 (02-01)

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POWER OF ATTORNEY OR AUTHORIZATION OF AGENT

Application Number	09/966,967
Filing Date	9/28/01
First Named Inventor	Xueping, Jiang
Title	Multiple Width Random Number
Group Art Unit	2121
Examiner Name	
Attorney Docket Number	7023-005

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I am the:

Applicant/Inventor.

Assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).

SIGNATURE of Applicant or Assignee of Record

Name	Mark L. Becker
Signature	
Date	9/23/02

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.

*Total of _____ forms are submitted.

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STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: Cerdelinx Technologies, Inc. (formerly known as Octillion Communications)

Application No./Patent No.: 09/966,967 Filed/Issue Date: 9/28/01

Entitled: Multiple Width Random Number Generation

Lattice Semiconductor Corporation, a Delaware Corporation
(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

- the assignee of the entire right, title, and interest; or
- an assignee of less than the entire right, title and interest.
The extent (by, percentage) of its ownership interest is _____ %

in the patent application/patent identified above by virtue of either:

A. [] An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

OR

B. [X] A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

1. From: Xueping Jiang, Ming Qu, Ji Zhao To: Octillion Communications, Inc.
The document was recorded in the United States Patent and Trademark Office at Reel 012218, Frame 0656, or for which a copy thereof is attached.
2. From: Cerdelinx Technologies, Inc. To: Lattice Semiconductor Corporation
The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.
3. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

[] Additional documents in the chain of title are listed on a supplemental sheet.

[X] Copies of assignments or other documents in the chain of title are attached.

[NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

9/23/02

Date

Mark L. Becker

Typed or printed name
Mark L. Becker_____
Signature_____
Associate General Counsel, IP_____
Title

PATENT ASSIGNMENT

WHEREAS, CERDELINX TECHNOLOGIES, INC., a California corporation located in San Jose, California and formerly known as OCTILLION COMMUNICATIONS, INC. ("Assignor") owns the U.S. patents and patent applications listed and described on the attached Schedule A (the "Patents"); and

WHEREAS, Assignor was recently acquired by LATTICE SEMICONDUCTOR CORPORATION, a Delaware corporation headquartered in Hillsboro, Oregon ("Assignee"), the acquisition including the Patents, and Assignee, as part of the acquisition, desires to have Assignor assign the Patents to Assignee:

NOW, THEREFOR, in consideration of the foregoing and for other good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, Assignor hereby irrevocably sells, assigns, conveys and transfers to Assignee, its successors and assigns, the entire right, title and interest in and to the Patents, anywhere in the world, including any patents issuing from or based upon all patent applications thereon, or any continuations, continuations-in-part, divisions, reissues or extensions of the Patents, and including any foreign counterparts of the Patents, and including, without limitation, the right to sue for and recover damages for any past, present or future infringement of such Patents, the same to be held and enjoyed by Assignee for its own use, and for its legal representatives and assigns, to the full end of the term for which patents are granted, as fully and entirely as the same would have been held by Assignor had this assignment and sale not been made.

Assignor agrees to execute any further papers and to do such other acts as may be necessary and proper, to vest full title in and to the Patents in Assignee.

EXECUTED this 6th day of September, 2002.

CERDELINX TECHNOLOGIES, INC.

By: 

Name: Martin R. Baker

Title: Secretary

ACKNOWLEDGMENT

STATE OF OREGON)
)
) ss.
COUNTY OF WASHINGTON)

This 6th day of September, 2002, before me personally came the above-named Martin R. Baker, who executed the foregoing instrument in my presence, and who acknowledged to me that he executed the same of his own free will for the purposes set forth therein.



Sharon Regala
Notary Public For Oregon

[SEAL]

SCHEDULE A

Patent Application Title	Patent App. No	Filing Date
Low Jitter PPL With Broad Tuning Range	09/870877	5/30/01
Low Voltage High Speed CMOS CML Latch and Mux Devices	09/881950	6/15/01
High Speed Data Sampling With Reduced Metastability	09/885243	6/19/01
Improved Coupling for LC-Based VCO	09/927289	8/9/01
Improved Buffering for LC-Based Stage	09/927612	8/9/01
Low Voltage Bandgap Reference Circuit	09/941454	8/28/01
Multiple Width Random Number Generator	09/966967	9/28/01
Lock Detector With Multiple Level Accuracy and Hysteresis	10/033555	12/27/01
Symmetrical CML Logic Gate System	10/066031	1/31/02